

10944 U.S. PTO  
09/739582

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Class	Subclass

ISSUE CLASSIFICATION

PATENT NUMBER

# U.S. UTILITY Patent Application

FA AK SCANNED	Q.T.P.E. G.H.	PATENT DATE
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APPLICATION NO. 09/739582	CONT/PRIOR	CLASS 257	SUBCLASS 758	ART UNIT 2815	EXAMINER Chen
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APPLICANTS

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David Overhauser  
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TITLE

Integrated circuit wiring architectures to support independent designs

PTO-2040  
12/99

## ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION								

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Primary Examiner) _____ (Date)			<b>ISSUE FEE</b>	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			<b>ISSUE BATCH NUMBER</b>	

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